FIG. 1A

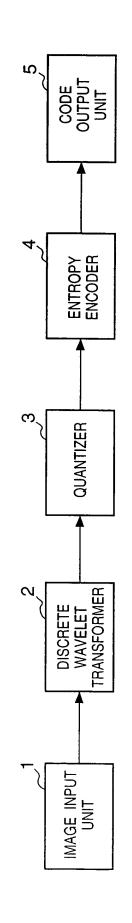


FIG. 1B

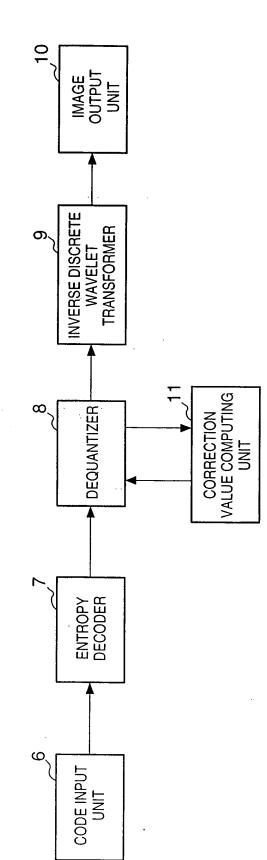


FIG. 2A

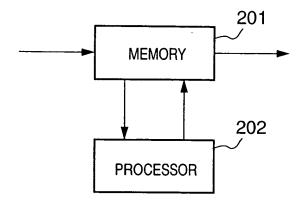


FIG. 2B

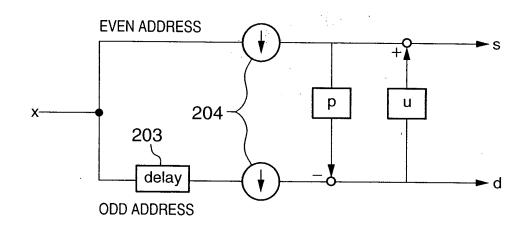
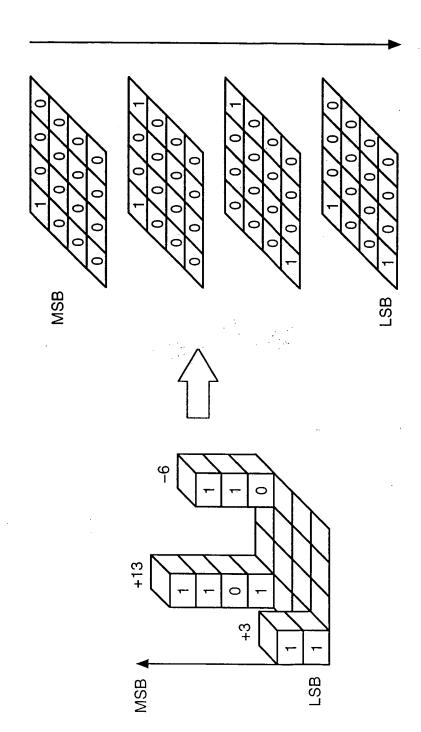


FIG. 2C

LL	HL2	HL1
LH2	HH2	F 1 L. 1
Lŀ	1 1	HH1

FIG. 3



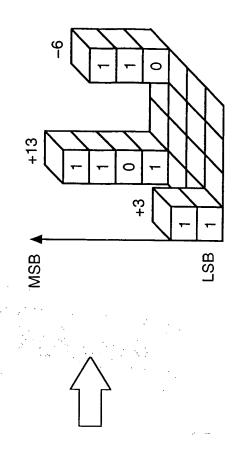
F1G. 4A	4 A	HW	TH0 BS0	王	BS1	THn-1 BSn-1	Sn-1
FIG.	4B	IMAGE SIZE	TILE SIZE	NOO NO	NUMBER OF COMPONENTS	COMPONENT INFORMATION	
FIG.	4C	TILE LENGTH	ENCODING PARAMETER	IG TER			
					-		
I	(BIT PLANE S-1	-1 BIT PLANE S-2	VE S-2		BIT PLANE 0	

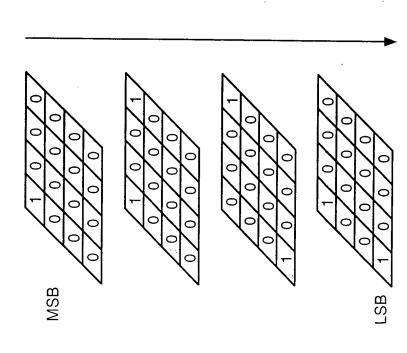
HL2 | LH2 | HH2 | HL1 | LH1 | HH1

LH2

HL2

FIG. 5





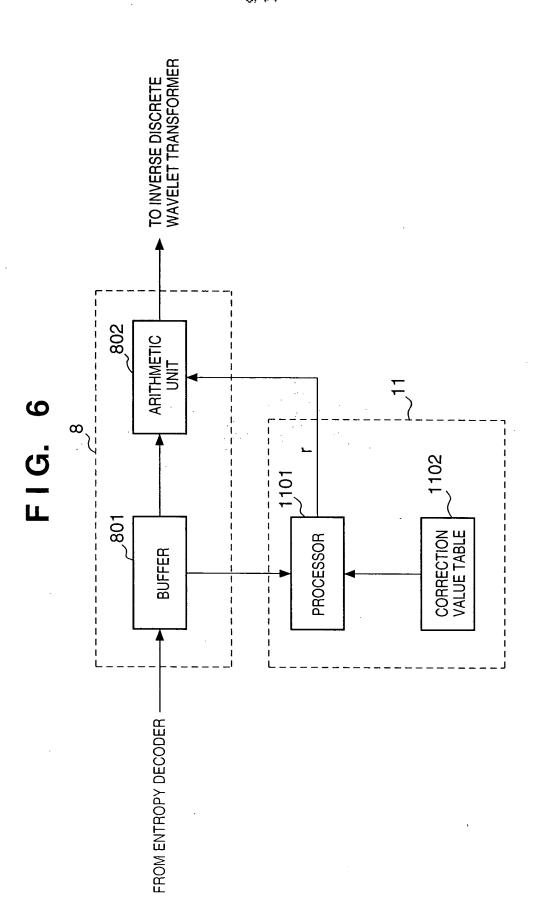


FIG. 7

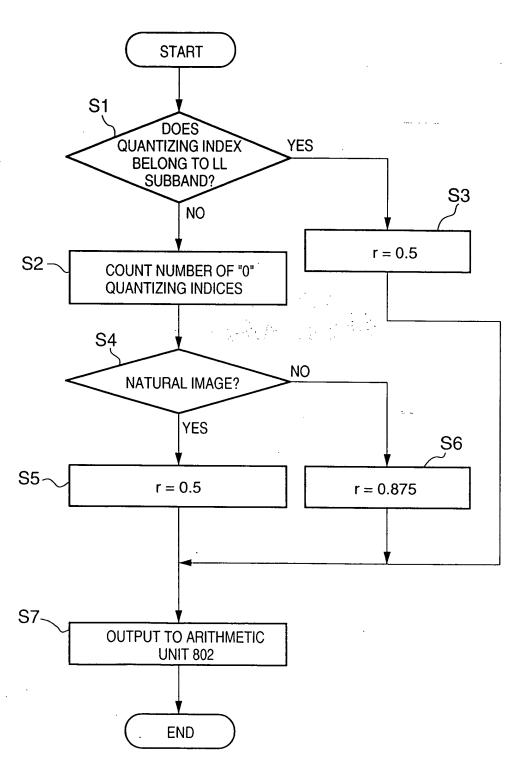


FIG. 8A

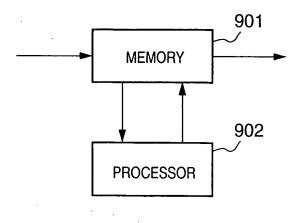
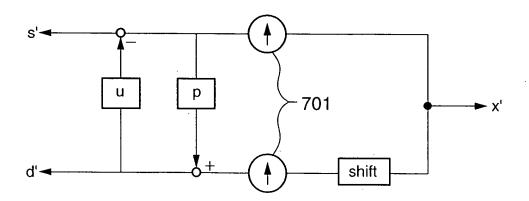


FIG. 8B



王 Ë HL2 LH2 HH2 HL1 8,12 BIT 0 FIG. 9A FIG. 9B 811 BIT S-2 BS0 님 HL2 LH2 THO 810 BIT S-1 Ξ

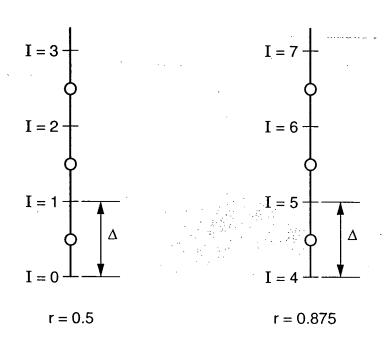


FIG. 10A F

FIG. 10B

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